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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/750,360	12/31/2003	Wen-Jyh Sah	250317-1080	4926
24504	7590	09/20/2005	EXAMINER	
THOMAS, KAYDEN, HORSTEMEYER & RISLEY, LLP			WANG, GEORGE Y	
100 GALLERIA PARKWAY, NW			ART UNIT	PAPER NUMBER
STE 1750				2871
ATLANTA, GA 30339-5948			DATE MAILED: 09/20/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/750,360	SAH ET AL. <i>(pm)</i>	
	<b>Examiner</b>	<b>Art Unit</b>	
	George Y. Wang	2871	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) Responsive to communication(s) filed on 23 June 2005.
- 2a) This action is FINAL.                    2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) Claim(s) 1-10 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) Claim(s) \_\_\_\_\_ is/are allowed.
- 6) Claim(s) \_\_\_\_\_ is/are rejected.
- 7) Claim(s) 1-10 is/are objected to.
- 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 31 December 2003 is/are: a) accepted or b) objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All    b) Some \* c) None of:
  1. Certified copies of the priority documents have been received.
  2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____ .	6) <input type="checkbox"/> Other: _____ .

## DETAILED ACTION

### *Election/Restrictions*

1. Applicant's election without traverse of claims 1-10 in the reply filed on June 23, 2005 is acknowledged. Note: Nonelected claims 11-12 were cancelled accordingly.

### *Claim Rejections - 35 USC § 102*

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1-10 are rejected under 35 U.S.C. 102(b) as being anticipated by Dohjo et al. (U.S. Patent No. 6,078,366, hereinafter "Dohjo").

4. As to claim 1, Dohjo discloses a manufacturing method of a transflective TFT-LCD panel (1) comprising the steps of forming a first conductive layer (col. 19, line 30) on a substrate (101), defining the first conductive layer to form a gate (113), forming a dielectric layer (115) on the gate, forming a channel over the gate (119, 121), forming a photo-resist block (170), forming a second conductive layer and defining it to form a source (126a) and drain (126b) and a photo-reflective layer (124b), forming a protection layer (127) and defining it to form a first opening to expose the drain (fig. 16, ref. 127)

and a second opening to expose the photo-reflective layer, and forming a transparent electrode (131) electrically connected to the drain and photo-reflective layer via the opening.

5. Regarding claims 2-4, Dohjo discloses the manufacturing method as recited above where a capacitor (111) is set under the photo-resist block (170) during the first step, where the first conductive layer is a first metal layer (col. 19, line 30) and the substrate is made of glass (101; col. 19, line 32).

6. As to claims 5-7, Dohjo discloses the manufacturing method as recited above where the photo-resist is positive (170), the second conductive layer is a second metal layer (col. 20, lines 20-33), and the transparent electrode is composed ITO (131; col. 7, line 40).

7. Regarding claims 8-10, Dohjo discloses the manufacturing method as recited above comprising the steps of forming a thin film transistor (TFT) (112) and a capacitor electrode (111) and forming a transparent electrode (131) within the transmissive area (“pixel area”), where the photo-reflective layer (124b) is formed above the capacitor electrode (111) and the photo-resist block (170) formed prior to the formation of the photo-reflective layer (col. 20, lines 19-41).

### ***Conclusion***

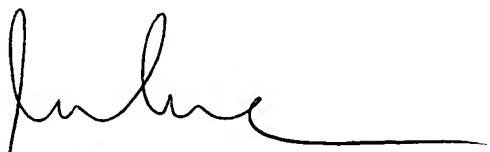
Art Unit: 2871

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to George Y. Wang whose telephone number is 571-272-2304. The examiner can normally be reached on M-F, 8 am - 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert H. Kim can be reached on 571-272-2293. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

gw  
September 19, 2005



DUNG T. NGUYEN  
PRIMARY EXAMINER